CLAIMS

5   1.   Apparatus for processing data, said apparatus comprising:

a result data value generating circuit operable to generate a result data value upon execution of a data processing operation; and

a data processing register to which said result data value is written; wherein

at least one data processing operation executed by said result data generating

10   circuit is a conditional write data processing operation for which a result data value is not written to said data processing register when non-write conditions are met; and further comprising

a trash register to which a result data value may be written upon execution of said conditional write data processing operation when said non-write conditions are

15   met.

2.   Apparatus as claimed in claim 1, comprising a register bank having a plurality of data registers to which result data values are written.

20   3.   Apparatus as claimed in any one of claims 1 and 2, wherein said result data generating circuit is part of a processor core responsive to data processing instructions specifying data processing operations to be performed.

4.   Apparatus as claimed in claim 3, wherein said data processing instructions

25   include one or more conditional data processing instructions and said processor core does not executed conditional data processing instructions when predetermined conditions are detected.

5.   Apparatus as claimed in claim 4, wherein if a conditional data processing

30   instruction includes a write operation and said predetermined conditions are detected, then said processor core performs a write operation to said trash register.

6.   Apparatus as claimed in any one of the preceding claims, wherein writing to said trash register is programmably disabled by a trash register control signal.

7.   Apparatus as claimed in claim 6, wherein said trash register control signal is stored in a system configuration register.

5   8.   Apparatus as claimed in claim 2, wherein said trash register is part of said register bank, said trash register being unmapped to a register number such that said trash register may not be specified by a register specifying operand value.

9.   A method of processing data, said method comprising the steps of:

10   generating a result data value upon execution of a data processing operation, at least one data processing operation executed being a conditional write data processing operation, wherein

a result data value is not written to a data processing register when non-write conditions are met but is instead written to a trash register.

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10.   A method as claimed in claim 9, wherein said data processing register is part of a register bank having a plurality of data registers to which result data values are written.

20   11.   A method as claimed in any one of claims 9 and 10, wherein said method is performed by of a processor core responsive to data processing instructions specifying data processing operations to be performed.

12.   A method as claimed in claim 11, wherein said data processing instructions

25   include one or more conditional data processing instructions and said processor core does not executed conditional data processing instructions when predetermined conditions are detected.

13.   A method as claimed in claim 12, wherein if a conditional data processing

30   instruction includes a write operation and said predetermined conditions are detected, then said processor core performs a write operation to said trash register.

14.   A method as claimed in any one of claims 9 to 13, wherein writing to said trash register is programmable disabled by a trash register control signal.

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15.   A method as claimed in claim 14, wherein said trash register control signal is stored in a system configuration register.

5   16.   A method as claimed in claim 10, wherein said dummy register is part of said register bank, said trash register being unmapped to a register number such that said trash register may not be specified by a register specifying operand value.